



Mahavir Education Trust's

Shah & Anchor Kutchhi Engineering College

An Autonomous Institute Affiliated to University of Mumbai

UG Program in Electronics and Communication
(Advanced Communication Technology)

DATE: 03-10-2024

Academic Year: 2024-25 (ODD Sem)

NOTICE

SECOND YEAR (SEMESTER III)

TIME TABLE FOR END SEMISTER PRACTICAL EXAMINATION

DATE/ SBJECT	Transforms, Trigonometric series & Vector Calculus Lab	Analog Circuits Lab	Skill Lab: Python Programming	Digital System Design with Verilog Lab
Time	9.00 AM TO 5.00 PM	9.00 AM TO 5.00 PM	9.00 AM TO 5.00 PM	9.00 AM TO 5.00 PM
21/10/2024	Roll No. 1-32	---	---	---
22/10/2024	---	---	---	---
23/10/2024	---	---	Roll No. 1-32	---
24/10/2024	---	---	Roll No. 33-65	---
25/10/2024	---	Roll No. 33-65	---	Roll No. 1-32
26/10/2024	Roll No. 33-65	---	---	---
27/10/2024	---	---	---	---
28/10/2024	---	Roll No. 1-32	---	Roll No. 33-65


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PRINCIPAL