



Mahavir Education Trust's

Shah & Anchor Kutchhi Engineering College

An Autonomous Institute Affiliated to University of Mumbai

UG Program in Electronics and Communication
(Advanced Communication Technology)

DATE: 03-03-2025
Academic Year: 2024-25 (ODD Sem)

NOTICE SECOND YEAR (SEMESTER III)

TIME TABLE FOR SUPPLEMENTARY END SEMESTER PRACTICAL EXAMINATION

DATE	TIME	SUBJECT
5/3/2025	11.00 AM TO 2.00 PM	Analog Circuits Lab
7/3/2025	11.00 AM TO 2.00 PM	Skill Lab: Python Programming
13/3/2025	11.00 AM TO 2.00 PM	Digital System Design with Verilog Lab


HOD


COE


PRINCIPAL



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TIME TABLE FOR SUPPLEMENTARY CONTINUOUS COMPREHENSIVE EVALUATION

DATE/SUBJECT	TIME	4/3/2025	6/3/2025	11/3/2025	15/3/2025
		COPONENT I		COPONENT II	
		Activity I	Activity II	Activity I	Activity II
Carrier Path Planning	10.00 AM to 11.00 AM	Quiz 1	Quiz 2	Assignment 1	Assignment 2
Analog Communication System	4.00 PM to 5.00 PM	Lab Assignment 1	Lab Assignment 2	Quiz 1	Quiz 2
Introduction to Artificial Intelligence	5.00 PM to 6.00 PM	Quiz 1	Quiz 2	---	Vlogging


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