



Mahavir Education Trust's

Shah & Anchor Kutchhi Engineering College

An Autonomous Institute Affiliated to University of Mumbai

UG Program in Electronics and Telecommunication Engineering
(Accredited by NBA for 3 years from AY 2024-25)

DATE: 25/09/2025

Academic Year: 2025-26 (Odd Sem)

TIME TABLE FOR INTERNAL ASSESSMENT KT TEST

| DATE | TIME | BE (SEM VII) 'C' Scheme |
|------------|----------------------|--|
| 06/10/2025 | 11.00 am to 12.00 pm | Microwave Engineering |
| 06/10/2025 | 2.30 Pm to 3.30 Pm | Mobile Communication System |
| 07/10/2025 | 11.00 am to 12.00 pm | Internet Communication Engg. |
| | | 5G Technology |
| 07/10/2025 | 2.30 Pm to 3.30 Pm | Efficient Architectures for DSP Algorithms |
| | | Deep Learning |
| | | Big Data Analytics |
| 08/10/2025 | 11.00 am to 12.00 pm | Cyber Security and Laws |
| | | Disaster Management & Mitigation Measures |

H. K. K.
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TIME TABLE FOR INTERNAL ASSESSMENT KT TEST

| DATE | TIME | BE (SEM VIII) 'C' Scheme |
|------------|----------------------|--|
| 06/10/2025 | 11.00 am to 12.00 pm | Optical Communication and Networks |
| 06/10/2025 | 2.30 Pm to 3.30 Pm | Natural Language Processing |
| | | Wireless Networks |
| 07/10/2025 | 11.00 am to 12.00 pm | Autonomous Vehicle |
| | | Satellite and Nano Satellite Communication |
| 07/10/2025 | 2.30 Pm to 3.30 Pm | Research Methodology |
| | | Environmental Management |


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| DATE | TIME | TE (SEM VI) 'C' Scheme |
|------------|----------------------|---|
| 06/10/2025 | 11.00 am to 12.00 pm | Electromagnetics and Antenna |
| 06/10/2025 | 2.30 Pm to 3.30 Pm | Image Processing and Machine Vision |
| 07/10/2025 | 11.00 am to 12.00 pm | Computer Communication Networks |
| 07/10/2025 | 2.30 Pm to 3.30 Pm | Database Management System |
| | | Mixed Signal VLSI |
| | | Digital Forensic |
| 08/10/2025 | 11.00 am to 12.00 pm | Artificial Neural Network and Fuzzy Logic |

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| DATE | TIME | TE (SEM V) 'C' Scheme |
|------------|----------------------|---------------------------------|
| 06/10/2025 | 11.00 am to 12.00 pm | Random Signal Analysis |
| 06/10/2025 | 2.30 Pm to 3.30 Pm | Digital VLSI |
| 07/10/2025 | 11.00 am to 12.00 pm | Digital Communication |
| 07/10/2025 | 2.30 Pm to 3.30 Pm | Discrete Time Signal Processing |
| 08/10/2025 | 11.00 am to 12.00 pm | Data Structures and Algorithm |
| | | Data Compression & Cryptography |
| | | Sensor Technology |


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| DATE | TIME | SE (SEM IV) 'C' Scheme |
|------------|----------------------|---|
| 06/10/2025 | 11.00 am to 12.00 pm | Microcontrollers |
| 06/10/2025 | 2.30 Pm to 3.30 Pm | Engineering Mathematics-IV |
| 07/10/2025 | 11.00 am to 12.00 pm | Linear Integrated Circuits |
| 07/10/2025 | 2.30 Pm to 3.30 Pm | Signals & Systems |
| 08/10/2025 | 11.00 am to 12.00 pm | Principles of Communication Engineering |


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TIME TABLE FOR INTERNAL ASSESSMENT KT TEST

| DATE | TIME | SE (SEM III) 'C' Scheme |
|------------|----------------------|---|
| 06/10/2025 | 11.00 am to 12.00 pm | Engineering Mathematics-III |
| 06/10/2025 | 2.30 Pm to 3.30 Pm | Electronics Devices & Circuits |
| 07/10/2025 | 11.00 am to 12.00 pm | Digital System Design |
| 07/10/2025 | 2.30 Pm to 3.30 Pm | Electronic Instrumentation & Control Systems |
| 08/10/2025 | 11.00 am to 12.00 pm | Network Theory |

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