Mahavir Education Trust's

Shah & Anchor Kutchhi Engineering College

An Autonomous Institute Affiliated to University of Mumbai

END SEMESTER EXAMINATION TIME TABLE

Winter-2026

PROGRAMME - Direct Second Year B. Tech in Electronics Engineering (VLSI Design and Technology)

SEMESTER - III

Days and Dates	Time	Paper Code	Paper
Saturday, 19 December,2026	11:00 a.m. to 01:00 p.m.	VDCOR2PC201	Electronic Devices and Circuits
Monday, 21 December,2026	11:00 a.m. to 01:00 p.m.	VDCOR2PC202	Digital System Design
Wednesday, 23 December,2026	11:00 a.m. to 01:00 p.m.	VDCOR2PC204	Advanced Engineering Mathematics

Important Note: • The candidates appearing for the examination should report 20 minutes before the start of examination.

• Mobile phones and other elctronic gadgets are prohibited in the examination hall.

• Change if any, in the time table shall be communicated on the college web site.

Dr. Aruna Sharma
Controller of Examinations

Dr. Bhavesh Patel
Principal

Shah & Anchor Kutchhi Engg. College Autonomous Institute Mumbai-88

Mumbai - 400 088
21th November, 2026